

ABOUT US

Founded in 1997 with the objective to serve OEM professional sales service

We have distribution network in automotive, computing, consumer products, military, etc.

We provide regional representation service and key account management service





ABOUT VIVIDSPARKS

VividSparks is a high-speed computing technology company that specializes in fabless semiconductors

They are oriented towards solving fundamental problems in arithmetic circuit designs

At VividSparks, their technology is built with the goal of delivering high performance to users with minimum power consumption possible





VIVIDSPARKS TECHNOLOGY

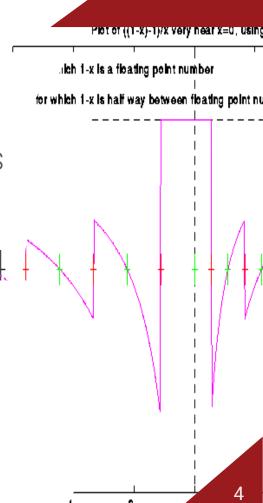
VividarithmetiK is a next generation arithmetic co-processor that computes basic arithmetic functions such as addition, subtraction, multiplication, division, square root, trigonometric functions, compare and convert instructions using a new number system called POSIT

- VividarithmetiK eliminates carry propagation in adders and multipliers using novel adder called Carry Free Adder (CFA)
- CFA not only eliminates carry propagation but also consumes very less power and exceptionally high performance
- They build top performing chips by implementing super-fast arithmetic libraries in them rather than fabricating chip in higher technology nodes



PROBLEM

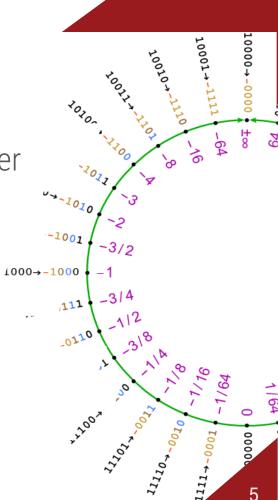
AI, HPC, machine learning, graphics computing, video processing applications demand high math computing with lesser data width, highly accurate result yet deliver higher performance.





SOLUTION

POSIT number system require half data width compared to floating point number system, more accurate results, large dynamic range, much reduced power consumption yet deliver higher formance and large dynamic range.





VividSparks F1 Products



High Performance Computing (HPC) Co-Processor

Provides server-class performance that is also unmatched in it's efficiency and size

- Designed for HPC applications undertaking complex computing tasks
- Provides support for high end mathematical functions

Provides more accurate results than Floating Point number system





POSIT GPU

Premium GPU architecture configurable up to 512 cores

- Flexible architecture with parameterization of different POSIT configurations
- More POSIT operations per watt
- Supports Floating Point equivalent POSIT instructions
- Supports CUDA equivalent programming model

VividSparks F1 Products



Artificial Intelligence (AI) Co-Processor

Smallest, low-cost co-processor for 16-bit

- Very light weight arithmetic units best suited for AI applications
- Support for matrix multiplications and additions for Neural Network
- Flexible architecture with parameterization of processing elements



RISC-V + Posit Co-processor Unit

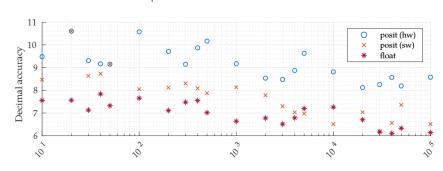
Cost efficient RISC-V+PCU that doesn't compromise performance

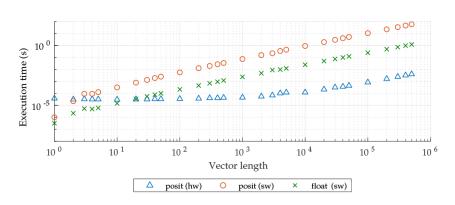
- ► Best suited for automotive applications
- Compatible for all RISC-V instructions
- Supported with VividSparks POSIT compiler

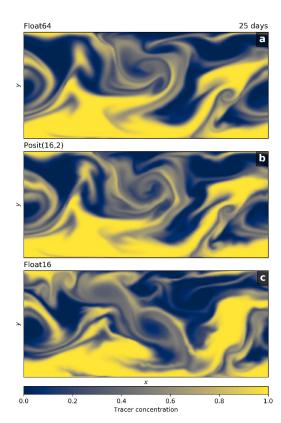


BENCHMARKS

DOT Product operation







Shallow water model



Competitive Comparison

	VIVIDSPAKRS Co-PROCESSOR (32 BIT)	INTEL FPU (32 BIT)	AMD FPU (32 BIT)	ARM FPU (32 BIT)	ANY OTHER FPU (32 BIT)
Number System	POSIT	FP	FP	FP	FP
Average Latency of Operations	5	30+	20+	20+	30+
EXECEPTIONS	3	8	8	8	8
DYNAMIC RANGE	2 × 10 ⁻⁷⁵ T0 5 × 10 ⁷⁴	7 × 10 ⁻⁴⁶ T0 3 × 10 ³⁸	7 × 10 ⁻⁴⁶ T0 3 × 10 ³⁸	7 × 10 ⁻⁴⁶ то 3 × 10 ³⁸	7 × 10 ⁻⁴⁶ T0 3 × 10 ³⁸
CARRY FREE COMPUTATION	YES	No	No	No	No



DIFFERENTIATION

Their co-processor are based on POSIT number system

Exceptionally high speed arithmetic libraries

Eliminates carry propagation in computation

Much better accurate result, large dynamic range and fewer exceptions than state-of-the-art Floating Point Units (FPUs)





FEATURES AND BENEFITS

All VividSparks' cores have the following capabilities to compute the following arithmetic operations with different POSIT configurations which are suited for different solutions.

- Sum of Products/2 independent products
- 2 independent Addition/Subtraction
- Multiply Add/Subtract
- Increment, Decrement
- 2 independent two's complement operations
- Sum of Reciprocals/2 independent reciprocals

- Sum of Inverse Square roots/2 independent Inverse Square roots
- Sum of Square roots/2 independent Square roots
- 2 independent trigonometric functions (sin, cos, tan, exp(x), log, etc.)
- 2 independent Integer to POSIT and vice versa conversions
- 2 independent compare operations
- Simple instruction pipeline



Thank you!

